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Phase Lock Basics Egan.pdf Types Phaselock characteristics Phaselock is generally viewed as the ability of a system to track the applied oscillator's frequency very accurately. In its most simple form, it is a feedback system that constantly compares the phase of the oscillator's output to that of the applied reference and drives the oscillator to be in phase with the reference as much as possible. While other synchronization terms may refer to the ability to create a lock, only a phase-lock is achieved if the oscillator's frequency tracks that of the applied reference exactly and reliably. A phase-lock in which there is no change in the frequency of the signal being phase-locked is called a phase-lock loop (PLL) or in some cases, if there is a reference change, a phase-lock loop with external frequency control (PLL-X). The same as the PLL but with a bandwidth limited phase-lock loop can be used to describe a phase-lock with no change in the frequency. It is achieved when the signal's output is in phase with the applied oscillator, but with little or no change in frequency. This is often achieved in a Phase detector. A sideband A sideband is a term used to describe the ability to produce a signal at a given frequency that is the sum of a signal at a given frequency and a second frequency derived from the first. This can be done with a mixer or with a synthesizer. In the case of a mixer, the signal to be mixed is fed into a mixer along with a local oscillator signal that is usually fixed or a band-limited. The product is then filtered so that the local oscillator signal is removed from the output signal and the output signal is at the sum of the two frequencies. In the case of a synthesizer, the signal to be generated is fed into a system that generates an at the desired output frequency, usually at a fixed or variable frequency. The output of the synthesizer can be fed into a mixer to produce a sideband. A simple phase detector The simplest phase detector has only the following functions: 1. The positive peak of the input signal is detected and used to trigger a pulse which is counted by an accumulator or accumulators. 2. The accumulator counts the pulses to produce a one-bit binary value. This produces a one-bit phase error signal 2d92ce491b